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AMENDMENT TRANSMITTAL LETTER

Docket No. SON-1285/DIV

Application No. Filing Date Examiner Art Unit 10/752,705-Conf. #5551 January 8, 2004 Jack S. J. Chen 2813

Applicant(s): Takashi Noguchi et al.

FIELD EFFECT TRANSISITOR AND FABRICATION THEREOF, SEMICONDUCTOR Invention: DEVICE AND FABRICATION THEREOF, LOGIC CIRCUIT INCLUDING THE

SEMICONDUCTOR DEVICE, AND SEMICONDUTOR SUBSTRATE

TO THE COMMISSIONER FOR PATENTS

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

		CLAIM	S AS AMENI	DED	
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	13	- 27 =		, x	0.00
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Multiple Depend	lent Claims (che	eck if applicabl	le)		İ
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	is hereby auth I below. A dup			Deposit Account I	No. <u>18-0013</u>
x Credit a	y overpaymen	nt.			
x Charge a	iny additional fili	ng or applicatio	n processing	ees required under	37 CFR 1.16 and 1.1
Ronald P. Kana				Dated:	February 20, 2007
Attorney Reg. N					
RADER, FISHM 233 20th Stree Suite 501 Vashington, DO		R PLLC			
	, 20000				



Docket No.: SON-1285/DIV

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Takashi Noguchi, et al

Conf. # 5551

Application No.: 10/752,705

Group Art Unit: 2813

Filed: January 8, 2004

Examiner: Jack S.J. Chen

For: FIELD EFFECT TRANSISTOR AND

FABRICATION THEREOF,

SEMICONDUCTOR DEVICE AND

FABRICATION THEREOF...

RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT (37 C.F.R. 1.121)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

This is in response to the Notice of Non-Compliant Amendment (37 C.F.R. 1.121) mailed on February 9, 2007.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 15 of this paper.